

JSS Mahavidyapeetha JSS Academy of Technical Education, Noida Department of Electronics and Communication Engineering.



FACULTY PROFILE

1. Personal Details

NAME	Dr. MANOJ JOSHI	
DEPARTMENT	ELECTRONICS AND COMMUNICATION ENGINEERING	
DESIGNATION	ASSISTANT PROFESSOR	
PHONE	8859916019	
EMAIL ID	manojjoshi@jssaten.ac.in	
Date of Joining (JSSATEN)	07/11/2022	100

2. Experience

Total Experience in Years: 11	Teaching: 05	Industry: 00	Research: 06	
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3. Qualifications

COURSES			INSTITUTION	UNIVERSITY
B.E.	ELECTRONICS AND COMMUNICATION ENGINEERING	2011	DIT Dehradun	UTU Dehradun
M.Tech.	ELECTRONICS AND COMMUNICATION ENGINEERING	2014	KEC DWARAHAT (Govt.)	UTU Dehradun
Ph.D.	ECE (VLSI Analog Circuit Design)	2021	National Institute of Technology Manipur	NIT
Post Doc.	-	-	-	-

4. Research & Publications

Papers Published in Web of Science indexed	International: 08	National: 00
Journals		
Papers Published in		
SCOPUS indexed	International: 01	National: 00
Journals		
Papers Published in	International: 00	National: 00
other Journals	international: 00	National: 00
Papers Presented in		
Conferences /	International: 07	National: 00
Symposium		
Books / Book chapters Published		06

5. Research Guidance

PhD Guide? Give field & University	No	University: NIL
PhDs / Projects Guided	Ph.D. Awarded: Guiding: NIL	Projects at Master's Level: -01 Projects at Bachelor's Level: 08

6. Grants

i. Funds Received (Projects)

Project Name	Grant Amount	receiving	Grant issuing authority/ Body / Organization	
-	-	-	-	-
-	-	-	-	-

ii. Patents

Sl. No.	Title Details	Details of award of patent / Filed
1	Method and system to provide an Enhanced cleaning	202211006622
	of laundries through a washing machine	(02-08-2022) filled
2	System and method to recognize a text present in	filled
2	an environment for visually impaired person	inicu
3	•	-
4	-	-

iii. Consultancy

Title of the work	Amount in Rs.	Date of receiving the Grant	Duration	Grant issuing authority/ Body / Organization
-	•	•	-	-

7. Awards Received

Evment Leetune	One week short term Course (STC) program on "Analog
Expert Lecture	Circuit Design using ORCAD: Hand on Training

8. Publications

i. International Journals

Sl. No		Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WO S/ Sco pus/ Bot h	Impa ct Facto r	Publisher
1.	Low power chaoti oscillator employir CMOS	_	Integration th VLSI Journal ISSN: 0167-920	2022, Pages 5	57- 16/	CI 1.2:	11 Elsevier
2.	A new high frequency MOS-0 TOETQO employin DXCCTA		m, Journal of j Electronics Letters i ISSN:2168-172	2022, Pages 5 520 https://doi.or	05- g/1 172	CI 1.33	Taylor and Francis
3.	Dual Feedback IRG Ring for Chaotic Waveform Generation	Mano Joshi, Ashish Ranjar	Devices & Systems	Volume 15 October 202 Pages 595-60 doi.org/10.104 ds2.12054	21 80	CI 1.29	97 IET
4.	Nth order Simple Hyperjerk System with unstable equilibrium and Its Application as RPG	nailiai	Systems, and Signal	5934, May 20 doi.org/10.10	21, 07/ SO	CI 2.22	25 Springer
5.	Investigation of dynamical properties in hysteresis-based simple chaotic waveform generator with twe stable equilibrium	0	& Fractals ISSN: 0960-07	134, May 202	3 <u>16/</u> So	CI 5.94	44 Elsevier
6.	An Autonomous Simple Chaotic Jer	Mano		Volume 30(5 Page 205007		CI 2.83	World Scientific

	System With Stable	Ashish	Bifurcation and	2020,			
	And Unstable	Ranjan	Chaos	doi.org/10.1142/			
	Equilibria Using		ISSN: 0218-1274	S0218127420500			
	Reverse Sine			704			
	Hyperbolic						
	Functions						
	New simple chaotic	Manoj	AEU -	Volume 108,			
	and hyperchaotic	Joshi,	International	Pages 1-9,			
7.	system with an	Ashish	Journal of	August 2019,	SCI	3.183	Elsevier
/•	unstable node	Ranjan	Electronics and	ISSN: 1434-8411	SCI		
			Communication				
			S				
	An autonomous	Manoj	Analog	February 2019,			
	chaotic and	Joshi,	Integrated	101(3), Pages			
8.	hyperchaotic	Ashish	Circuits and Sig	401-413,	SCI	1.337	Springer
0.	oscillator using OTRA	Ranjan	nal Processing	doi.org/10.1007/	SCI	1.557	Springer
			ISSN 1573-1979	s10470-019-			
				01395-0			

ii. National Journals

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus / Both	Impact Factor	Publisher
01	Designing of Chebyshev Higher Order Filter using OTRA	Vivek Bhatt, S khantwal, Ravi Negi, Manoj Joshi	International Journal of Recent Technology and Engineering	2020, 8, 2163-2167, ISSN:2277- 3878	Scopus	-	-

iii. Conferences

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Conference	Volu me No. Issue No. Year	WOS / Scopus	Imp act Fact or	Publisher
1.	A new High Order Electronically Tunable Oscillator (HO-ETO)	Manoj Joshi, Rakesh Kumar, Mohan Singh and Shilpa Choudhary	1 st International Conference on Science, Technology and Engineering (ICSTE 23) (Feb 2023)	1	Scopus	-	Springer

2.	Design and Implementation of Third Order Oscillator using CFOA and OTA	Manoj Joshi, Kirti	International Conference on Electrical, Computer and Energy Technologies Taj Cape Town, Cape Town, South Africa, 10.1109/ICECET52533.2021 .9698785	09 - 10 Dec 2021	Scopus	-	IEEE
3.	Realization of Higher- Order Hyperchaotic Jerk System	Manoj Joshi, Vivek Bhatt	First International Virtual Conference on Integrated Intelligence Enable Networks & Computing, Institute of Technology, Gopeshwar, India	May 25-27, 2020	Scopus		Springer
4.	Current Controlled Chaotic Chua's Circuit using CCCII	Manoj Joshi, Ashish Ranjan	First International Conference on Advanced Communication & Computational Technology, NIT, Kurukshetra, India	Dec 6-7, 2019	Scopus		Springer
5.	A Current Tunable Third Order Oscillator Using CCDDCC	Sunil Kumar Yadav, Manoj Joshi, Ashish Ranjan	3th International Conference on Micro- Electronics and Telecommunication Engineering, SRM University, Delhi NCR	28-29 Sep, 2019	Scopus		Springer
6.	Realization of Novel multi-scroll 2D Chaotic Oscillator Using DVCC	Manoj Joshi, Ashish Ranjan	International Conference on Manufacturing, Advance Computing, Renewable Energy and Communication, HMR Institute of Technology and Management, Delhi, India	July 19 th - 20 th 2018	Scopus		Springer
7.	OTRA Based Second- Order Universal Filter and its optimization like Butterworth, Chebyshev, and Bessel	Vivek Bhatt, Pramod Benjwal, Manoj Joshi	Second International Conference on Research in Intelligent and Computing in Engineering, Gopeshwar, India	March 24– 26, 2017	Scopus		
8.	Realization of Colpitts oscillator using second- generation current controlled current conveyor	Manoj Joshi, Vivek Bhatt, Ashish Ranjan, Pramod Benjwal	Second International Conference on Research in Intelligent and Computing in Engineering, Gopeshwar, India	March 24– 26, 2017	Scopus		

22- 23, Scopus	Feb 22- 23, 2014	ternational in Computing & Communication ngineering (ICACCE-2014) BTKIT Dwarahat, India	Ashish Ranjan, Vivek Bhatt, Manoj Joshi	Realization of third- order low pass filter and its nature like Butterworth, Bessel and Chebyshev using OTRA	9.
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iv. Workshops/Conferences Attended

Sl. No.	Name of the workshop / Conference	Organizer	Date
1.	Python 3.4.3 Training	Spoken Tutorial Project, IIT Bombay	2023-01-03
2.	FDP on Embedded System and IoT	STMicroelectronics, India	10,20, 22 April 2022
3.	A2Z of NBA Accreditation Process	IEC Ghaziabad	Aug 24 th to Sept 09 th 2021
4.	Participated in the IDO-USA online Short Term Course on Approaches to DSP Architecture Optimization	SPARC- Ministry of Education.	14-17 July, 2021
5.	Reforms in Indian Education System Through NEP-2020	GLBITM Greater Noida	16 to 18 Dec 2021
6.	Trends in Electronics and Communication Engineering	NIT Manipur	27 t0 31 Dec 2020
7.	Device Modelling using synopsis, NIT Manipur	NIT Manipur under TEQIP	16-20 Nov. 2019
8.	VLSI Design Flow Using Cadence Suite, NIT Manipur	E&ICT- IIT Guwahati & NIT Manipur	25-29 March. 2019
9.	NKN course on "Wireless and Mobile Communication" NIT Manipur	E&ICT- IIT Guwahati, & NIT Manipur	3 -7 Dec. 2018
10.	NKN course on "DSP and Sensor", NIT Manipur	E&ICT- IIT Guwahati & NIT Manipur	10-15 Dec.2018
11.	Fractional order System, IIT Kharagpur	IIT Kharagpur	16-20 Feb.2018

v. Workshops / Conference (Organized)

l. No.	Name of the workshop / Conference	Organized by	Date	Role
1	INTERNATIONAL	GL Bajaj institute of Technology and Management greater Noida	Oct 29 th - 31 st , 2021	Organizing Committee

Modern Electronics Devices and		
Communication systems		
MEDCOM 2021		

vi. Conference Attended (those sponsored by AICTE / ISTE/IETE/TEQIP or any other sponsoring body)

Sl. No.	Name of the workshop / Conference	Organizer	Date
1.	Design and Implementation of Third Order Oscillator using CFOA and OTA	International Conference on Electrical, Computer and Energy Technologies Taj Cape Town, Cape Town, South Africa, 10.1109/ICECET52533.2021.969878 5)9 - 10 Dec 2021
2.	Realization of Higher-Order Hyperchaotic Jerk System	First International Virtual Conference on Integrated Intelligence Enable Networks & Computing, Institute of Technology, Gopeshwar, India	May 25-27, 2020
3.	A Current Tunable Third Order Oscillator Using CCDDCC	3th International Conference on Micro-Electronics and Telecommunication Engineering, SRM University, Delhi NCR	28-29 Sep, 2019
4.	Realization of Novel multi- scroll 2D Chaotic Oscillator Using DVCC	International Conference on Manufacturing, Advance Computing, Renewable Energy and Communication, HMR Institute of Technology and Management, Delhi, India	July 19 th - 20 th 2018
5.	OTRA Based Second-Order Universal Filter and its optimization like Butterworth, Chebyshev, and Bessel	Second International Conference on Research in Intelligent and Computing in Engineering, Gopeshwar, India	March 24–26, 2017
6.	Realization of Colpitts oscillator using second- generation current controlled current conveyor	Second International Conference on Research in Intelligent and Computing in Engineering, Gopeshwar, India	March 24–26, 2017
7.	Realization of third-order low pass filter and its nature like Butterworth, Bessel and Chebyshev using OTRA	International in Computing & Communication Engineering (ICACCE-2014) BTKIT Dwarahat, India	eb 22- 23, 2014

9. Details of NPTEL / COURSERA courses completed

l. No.	lame of the subject	Organized by	Date of completion / Award	Grade / Marks
1				

10. Membership of Professional Bodies:

Institute of Electronics and Electrical Engineers (IEEE)

11. Any other information you will like to share about your professional experience

GATE EXAM qualified in Electronics & Communication Engineering.